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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Goren et al	Examiner:	Naum B Levin
Serial No.:	10/091,934	Group Art Unit:	2825
Filed:	03/06/2002		
Title:	AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED CIRCUIT DESIGN		

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

This Amendment is filed in response to the Restriction Requirement dated **January 30, 2007** issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the **January 30, 2007** Office Action is due **March 2, 2007**. Accordingly, this Amendment is being timely filed.